



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Olszewski et al.**

Serial No.: **09/870,609**

Filed: **May 31, 2001**

For: **Run Queue Optimization with  
Hardware Multithreading for Affinity**

**35525**

PATENT TRADEMARK OFFICE  
CUSTOMER NUMBER

§ Group Art Unit: **2127**

§

§ Examiner: **Vo, Lilian**

§

§ Attorney Docket No.: **AUS920000844US1**

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By:

*Dell Whitton*  
Dell Whitton

**TRANSMITTAL DOCUMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

ENCLOSED HEREWITH:

- Copy of International Search Report;
- Information Disclosure Statement;
- Form PTO-1449;
- References AA-AE; and
- Our return postcard.

No fees are believed to be necessary. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to Deposit Account No. 09-0447. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to Deposit Account No. 09-0447.

Respectfully submitted,

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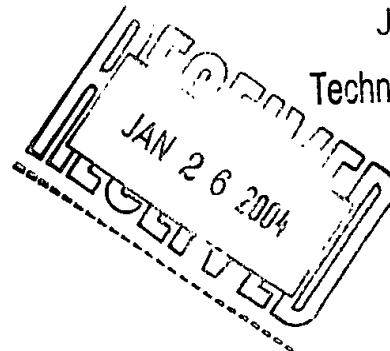
(972) 367-2001

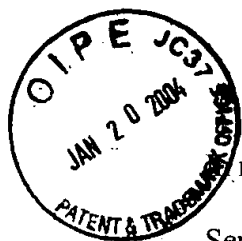
ATTORNEY FOR APPLICANTS

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**INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. 1.97**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants request that the information listed on the attached Form PTO-1449 be considered by the Office during the pendency of the above entitled application, pursuant to 37 C.F.R. 1.97.

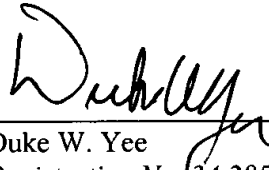
Please charge any fees necessary for prosecution of the present application to Deposit Account No. 09-0447. If any extension of time is required, such extension is hereby requested. Please charge any additional required fee for extension of time to Deposit Account No. 09-0447.

In accordance with 37 C.F.R. 1.97(h), the filing of this Information Disclosure Statement shall not constitute an admission that any information cited therein is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56(b). In the interest of full and complete disclosure to the Office, some or all of the art cited herein may not be considered by Applicant(s) or the Undersigned to be material under the new standards of materiality defined in 37 C.F.R. 1.56(b), enacted March 16, 1992, but may be material under the old standard of materiality defined in 37 C.F.R. 1.56(a), last amended on November 28, 1988, or may merely be technical background which may be of interest to the Examiner. In accordance with 37 C.F.R. 1.97(g), the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made.

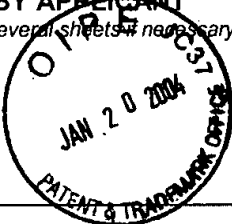
This Information Disclosure Statement is being filed under 37 C.F.R. § 1.97(b) before the mailing date of a first office action on the merits or each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. No fee is required.

Date: 11/2/04

Respectfully submitted,



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<b>Form PTO-1449</b>  <b>LIST OF PRIOR ART CITED BY APPLICANT</b> <i>(Use several sheets if necessary)</i>		ATTORNEY DOCKET NO. <b>AUS920000844US1</b>		SERIAL NO. <b>09/870,609</b>	
		APPLICANT <b>Olszewski et al.</b>			
		FILING DATE <b>May 31, 2001</b>		GROUP ART UNIT <b>2127</b>	
<b>U.S. PATENT DOCUMENTS</b>					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	INVENTOR NAME	CLASS/ SUBCLASS	FILING DATE
<b>FOREIGN PATENT DOCUMENTS</b>					
EXAMINER INITIAL	DOCUMENT NO.	PUBLICATION DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES NO
	<b>AA EP 0 827 071 A2</b>	<b>Mar. 4, 1998</b>	<b>Europe</b>	<b>G06F 9/38</b>	<b>X</b>
<b>OTHER PRIOR ART (including author, title, date, pertinent page, etc.)</b>					
	<b>AB</b>	<b>Lo et al., "Software-Directed Register Deallocation for Simultaneous Multithreaded Processors", IEEE Transactions on Parallel and Distributed Systems, Vol. 10, No. 9, Sept. 1999, pp. 922-933.</b>			
	<b>AC</b>	<b>Zilles et al., "The Use of Multithreading for Exception Handling", Proceedings of the 32<sup>nd</sup> Annual ACM/IEEE International Symposium on Microarchitecture. Haifa, Israel, Nov. 1999, Proceedings of the Annual ACM/IEEE International Symposium on Microarchitecture, Los Alamitos, CA, IEEE Comp. Soc, US, Nov. 1999, pp. 219-229.</b>			
	<b>AD</b>	<b>Wallace et al., "Threaded Multiple Path Execution", Computer Architecture, 1998, pp. 238-249.</b>			
	<b>AE</b>	<b>Farcy et al., "Improving Single-Process Performance with Multithreaded Processors", Proceedings of the 1996 International Conference on Supercomputing. Philadelphia, May 1996, pp. 350-357.</b>			
<b>RELATED PATENT APPLICATIONS</b>					
EXAMINER INITIAL	APPLICATION NO./ ATTY. DOCKET NO.	APPLICANT	TITLE		FILING DATE
<b>DATE CONSIDERED</b>			<b>EXAMINER</b>		
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.					